

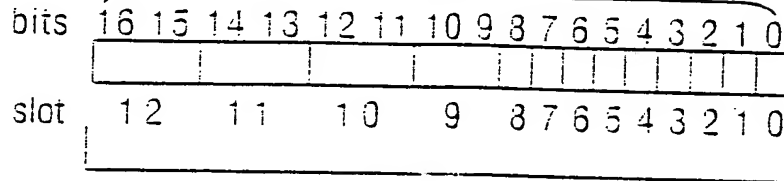


08

50 CP_VCD_LO

15-bits 17-bits

0 value



08

60 CP_VCD_HI

16-bits 18-bits

0 value

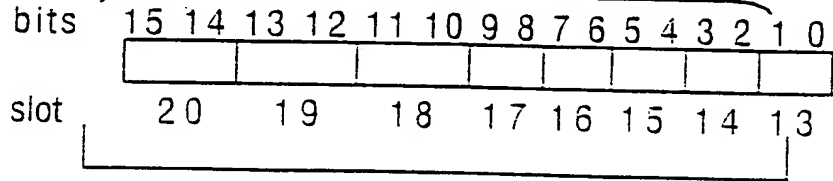
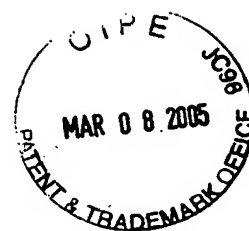


Fig. 12

GX SET VTX ATTR ATTR



GX SET VTX ATTR ENTRIES

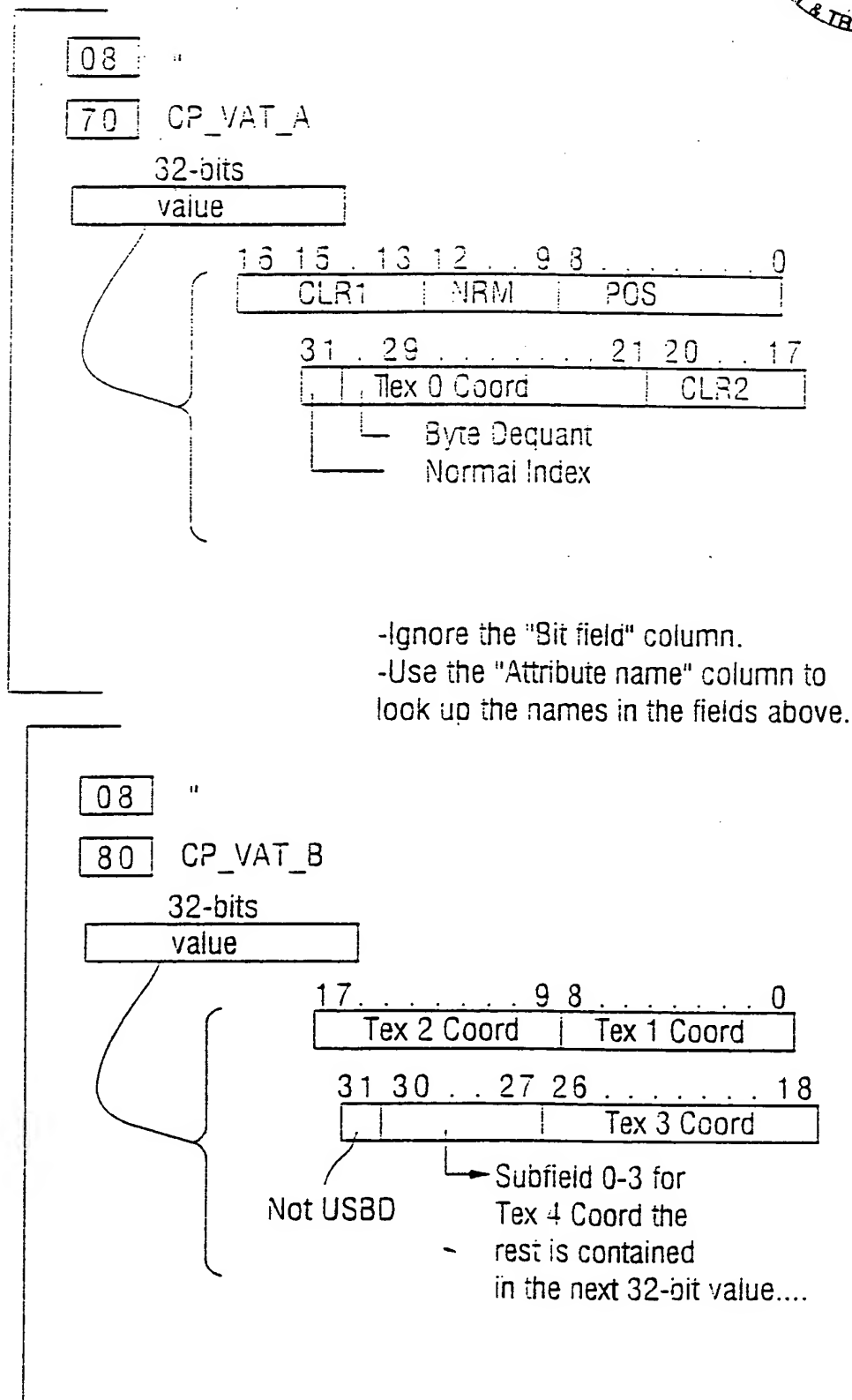


Fig. 13A